

Agilent Technologies

RF Balanced Device Characterization

January 15th, 2002

See the later presentations done Jan 14, 2003 and Jan 23, 2003 www.agilent.com/find/rfbalance

> presented by: Greg Amorese

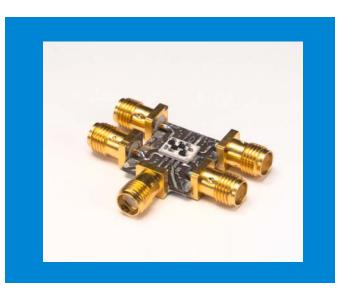
Agenda

- Balanced Device Overview
- Measurement Alternatives
- Mixed-Mode S-parameters
- Balanced Circuit Design Methodology
- Solution Overview
- Calibration
- Measurement Examples
- Conclusion



Balanced Device Market Situation

- Many devices for both RF design and high-speed digital applications
- Major benefits
 - High noise immunity
 - Low radiated noise
 - High density
 - Lower power consumption
- Technology used in new LTCC modules





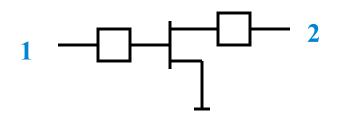


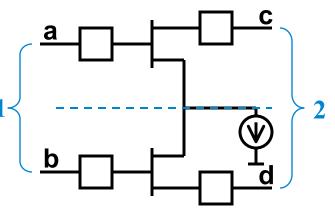
Device Evaluation Challenges

- Measurement tools are mostly unbalanced
- Traditional S-parameters provide limited insight into devices actual behavior
- No balanced VNA calibration standards
- No balanced RF connector standards
- No standard reference impedance (Z₀) for balanced devices



Differential Device Topology





Unbalanced Device

 Signals referenced to ground

Differential Device

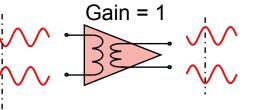
- Signals equal amplitude and anti-phase
- Also supports a common mode (in-phase) signal
- Virtual ground

Ideal Balanced Device Characteristics

Ideally, respond to differential and reject common-mode signals

Differential-mode signal

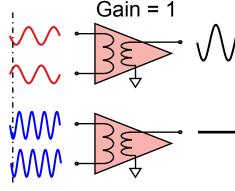
Common-mode signal (EMI or ground noise)



Fully balanced

Differential-mode signal

Common-mode signal (EMI or ground noise)



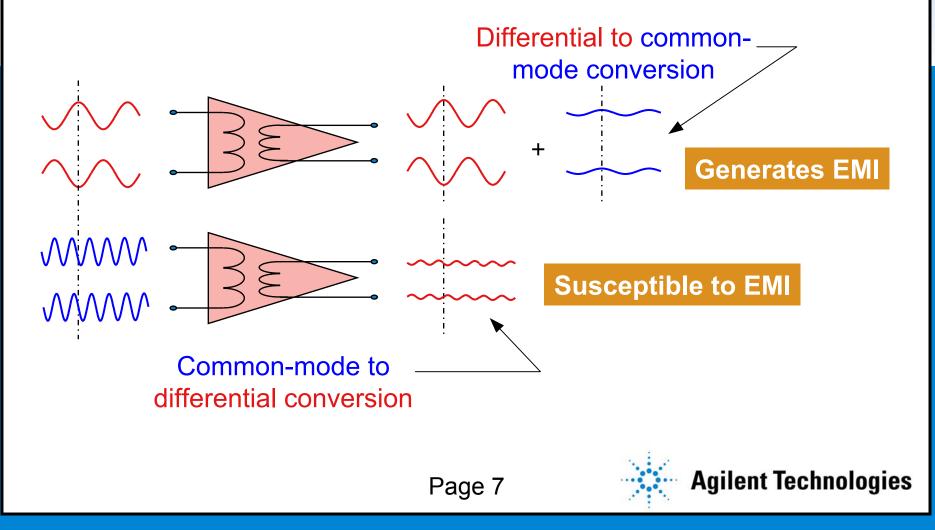
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Balanced to single ended



What About Non-Ideal Balanced Devices?

Mode conversions occur...

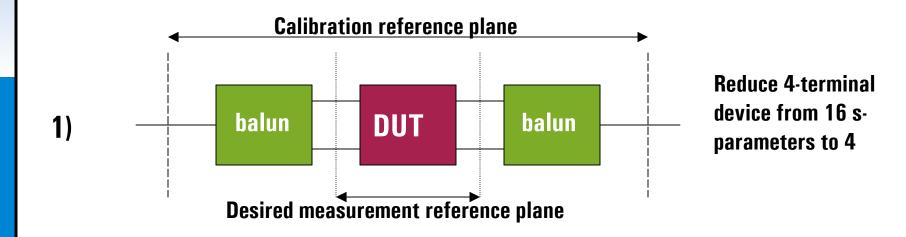


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Measurement Alternatives

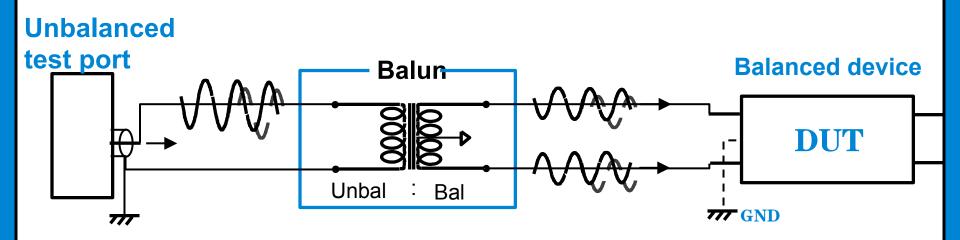






Mode Conversion (Unbalance <=> Balance)

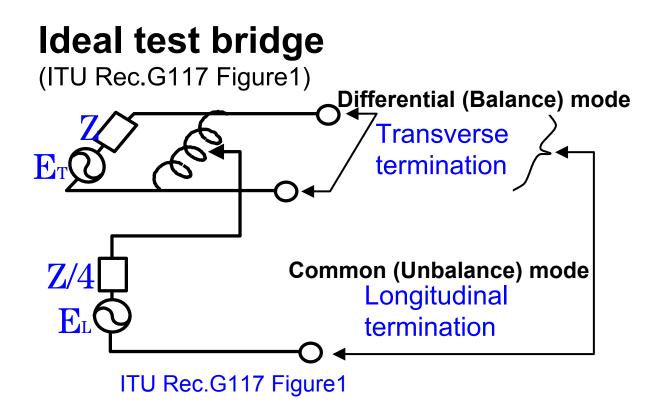
Need BALUN (BALanced/UNbalanced) transformer



BALUN transformer is **NOT** an ideal test bridge

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Definition



Ideal test bridge : lossless infinite-inductance centertapped coils

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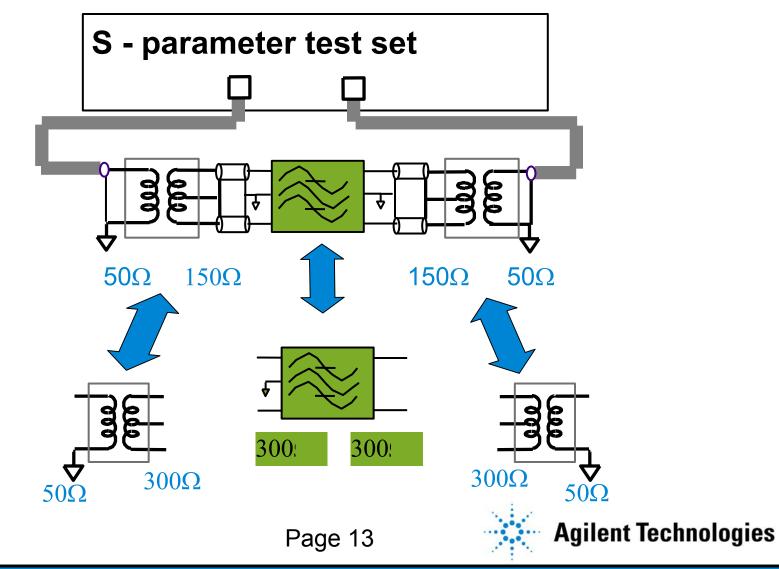
RF Balanced Device Characterization Balanced Device Test Parameters Port B1 BAL OF BAL Port B2 Port U1 UNBAL OF DUT OF BAL Port B2

Characteristic Impedance (Zc) : [Balanced], [Unbalanced] Input Impedance (Zin) : [Balanced], [Unbalanced] Insertion Loss (Attenuation) : SB2B1 [Balanced], SU2U1 [Unbalanced] Return Loss : SB1B1 [Balanced], SU1U1 [Unbalanced]

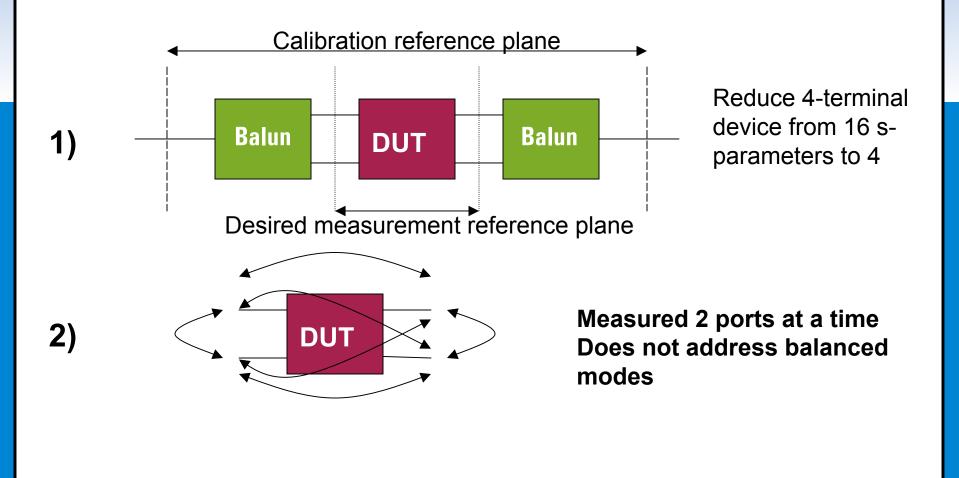
Conversion Loss, Rejection Ratio : (Balance to Unbalance Parameters) TCTL (Transverse Conversion Transfer Loss) : SU2B1 LCTL (Longitudinal Conversion Transfer Loss) : SB2U1 LCL (Longitudinal Conversion Loss) : SB1U1 CMRR (Common Mode Rejection Ratio)



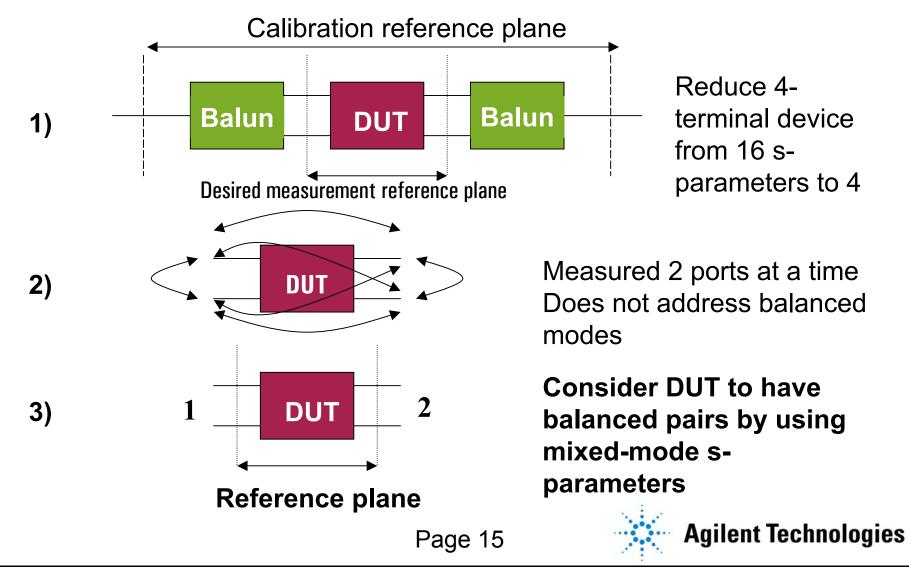
Calibration using Baluns



Measurement Alternatives



Measurement Alternatives



Agenda

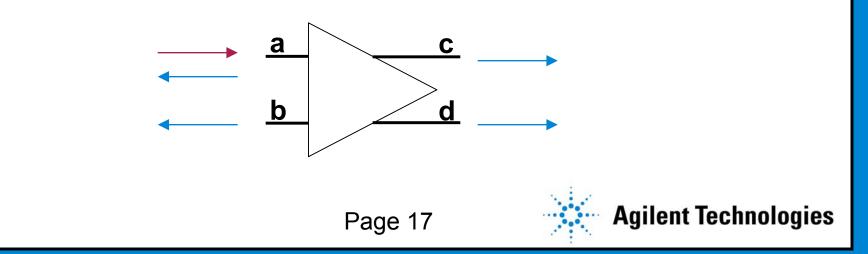
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Single-Ended S-Parameters

Conventional S-parameters answer the question ...

If a single port of a device is stimulated, what are the corresponding responses on all ports of the device?



Single-Ended S-Parameter Review

Single-ended 4-port



	Normalized power waves			
stimulus	$an = \frac{1}{2 \cdot \sqrt{\operatorname{Re} \{Zn\}}} (Vn + In \cdot Zn)$			
response	$bn = \frac{1}{2 \cdot \sqrt{\operatorname{Re} \{Zn\}}} (Vn - In \cdot Zn)$			

S=b/a

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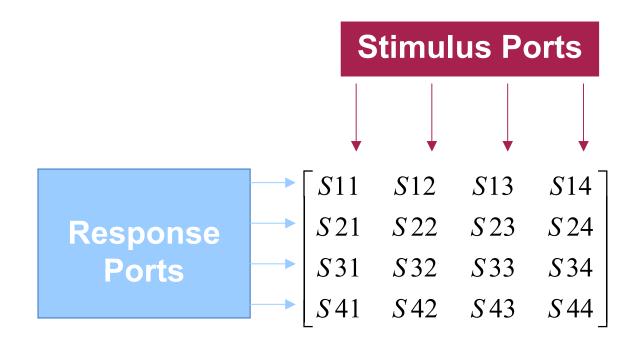


Vn

In

Single-Ended S-Matrix

S=b/a

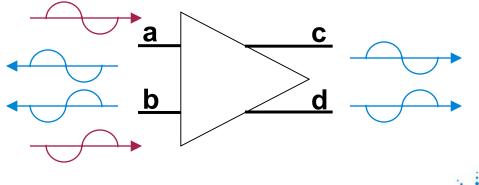




Mixed-Mode S-Parameters

Mixed-mode S-parameters answer the question ...

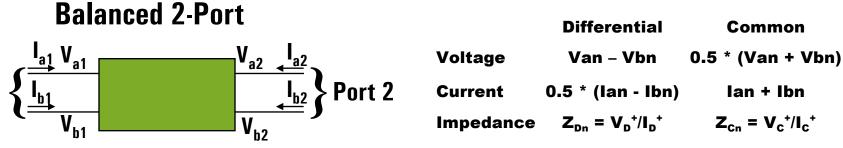
If a balanced port of a device is **stimulated** with a common-mode or differential-mode signal, what are the corresponding common-mode and differential-mode **responses** on all ports of the device?



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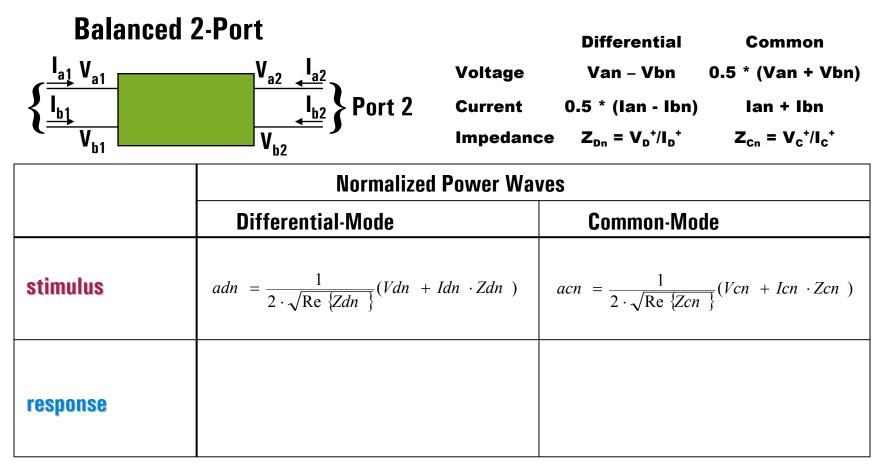
Mixed-Mode S-Parameters



	Normalized Power Waves				
	Differential-Mode	Common-Mode			
stimulus					
response					



Mixed-Mode S-Parameters



S = b/a



Mixed-Mode S-Parameters

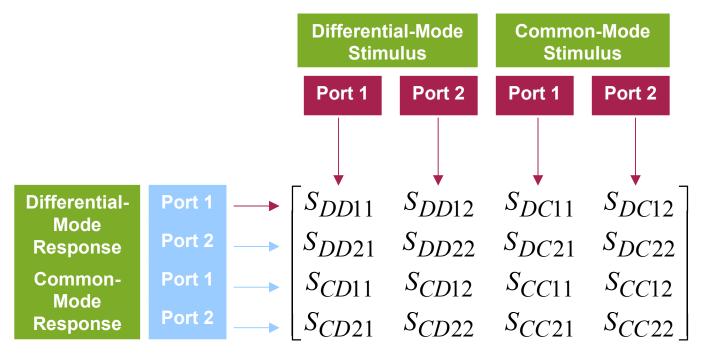
Balanced 2-Port			Differential	Common		
$\left\{ \underbrace{\frac{I_{a1}}{I_{b1}}}_{V_{a1}} \right\}$	$\left\{ \begin{array}{c} V_{a2} \\ I_{a2} \\ I_{b2} \end{array} \right\} $ Port 2	Voltage Current	Van – Vbn 0.5 * (Ian - Ibn)			
$V_{b1} \qquad V_{b2} \qquad Impedance Z_{Dn} = V_{D}^{+}/I_{D}^{+} \qquad Z_{cn} = V_{c}^{+}/I_{c}^{+}$ Normalized Power Waves						
	Differential-Mode		Common-Mode			
stimulus	$adn = \frac{1}{2 \cdot \sqrt{\operatorname{Re}\left\{Zdn\right\}}}(Vdn + Idn \cdot Zdn)$		$acn = \frac{1}{2 \cdot \sqrt{\operatorname{Re}\left\{Zcn\right\}}} (Vcn + Icn \cdot Zcn)$			
response	$bdn = \frac{1}{2 \cdot \sqrt{\operatorname{Re}\left\{Zdn\right\}}} (Vdn -$	Idn · Zdn)	$bcn = \frac{1}{2 \cdot \sqrt{\text{Re} \{Zcn\}}}$	$= (Vcn - Icn \cdot Zcn)$		

S = b/a



Mixed-Mode S-Matrix

S=b/a

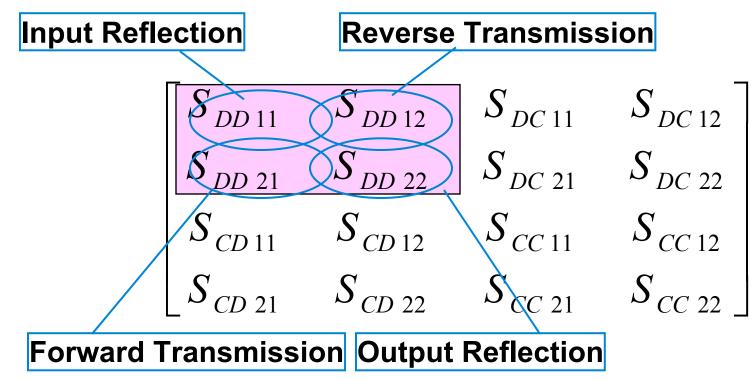


Naming Convention: S mode res., mode stim., port res., port stim.

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Mixed-Mode S-Matrix: DD Quadrant

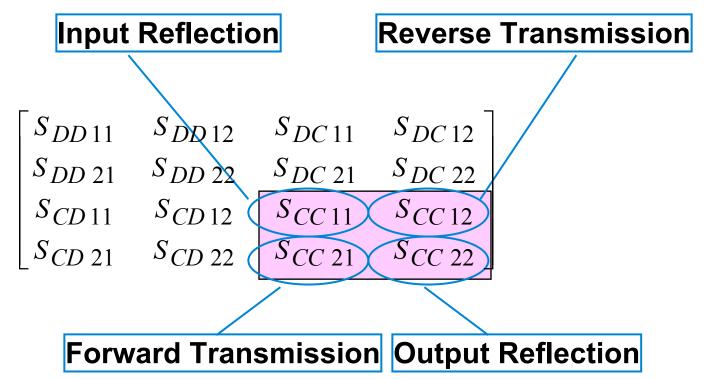


Describes fundamental performance in pure differential-mode operation

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Mixed-Mode S-Matrix: CC Quadrant

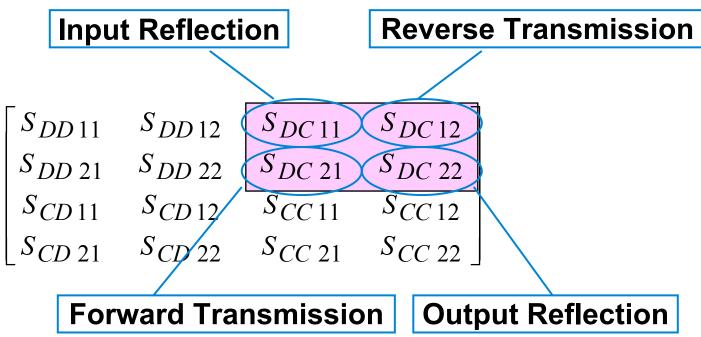


Describes fundamental performance in pure common-mode operation

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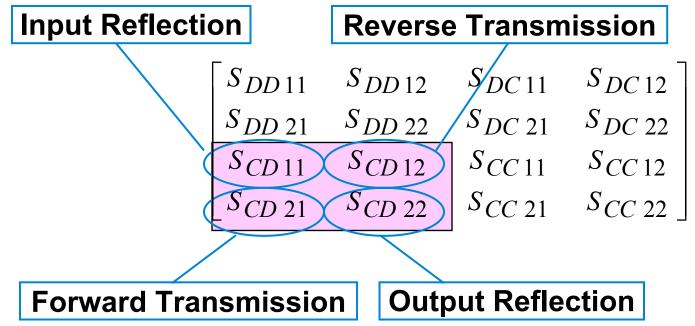
Mixed-Mode S-Matrix: DC Quadrant



- Describes conversion of a common-mode stimulus to a differential-mode response
- Terms are ideally equal to zero with perfect symmetry
- Related to the susceptibility to EMI



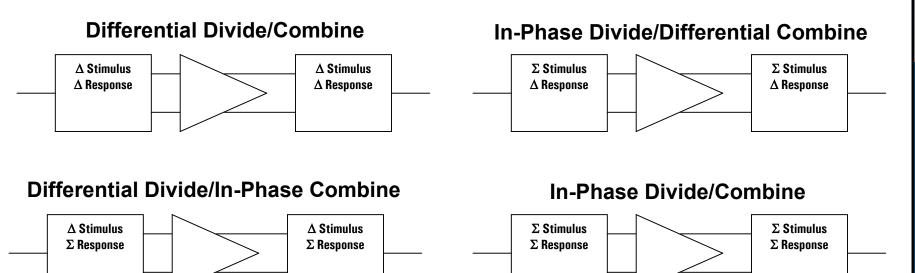
Mixed-Mode S-Matrix: CD Quadrant



- Describes conversion of a differential-mode stimulus to a common-mode response
- Terms are ideally equal to zero with perfect symmetry
- Related to the generation of EMI



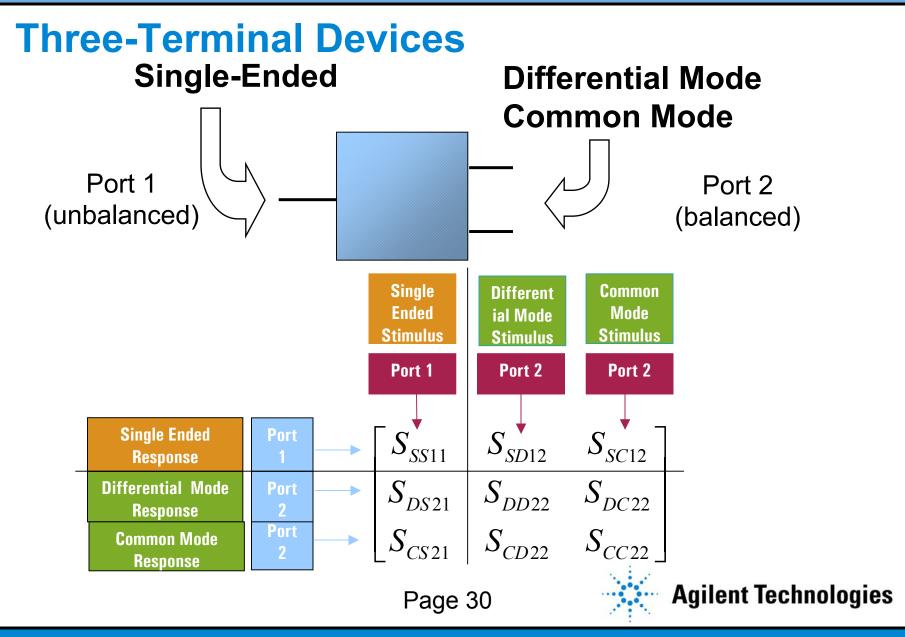
Conceptual Hardware Networks Required to Get Mixed-Mode S-Parameters with 2-port NA



DC and CD Quadrants would require complex hardware networks

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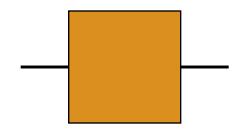
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Brain Teaser #1

What are the simultaneous conjugate input and output matching impedances of the following circuit?

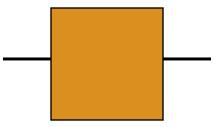


Single-ended 2-port



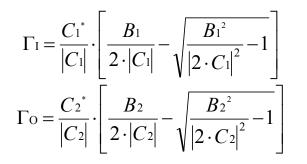
Brain Teaser #1: Answers

What are the simultaneous conjugate input and output matching impedances of the following circuit?



Well-documented relationship between simultaneous conjugate match and s-parameters.

Single-ended 2-port



where:

$$B_{1} = 1 - |S_{22}|^{2} + |S_{11}|^{2} - |D|^{2}$$

$$B_{2} = 1 - |S_{11}|^{2} + |S_{22}|^{2} - |D|^{2}$$

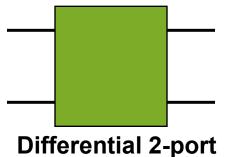
$$C_{1} = S_{11} - D \cdot S_{22}^{*}$$

$$C_{2} = S_{22} - D \cdot S_{11}^{*}$$

$$D = S_{11} \cdot S_{22} - S_{12} \cdot S_{21}$$
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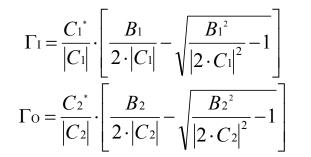
Brain Teaser #2: Answers

What are the simultaneous conjugate input and output matching impedances of the following circuit?



circuit to a single mode of operation using mixed-mode s-parameters, and follow same procedure as single-ended 2-port.

where:



$$B_{1} = 1 - |S_{DD22}|^{2} + |S_{DD11}|^{2} - |D|^{2}$$

$$B_{2} = 1 - |S_{DD11}|^{2} + |S_{DD22}|^{2} - |D|^{2}$$

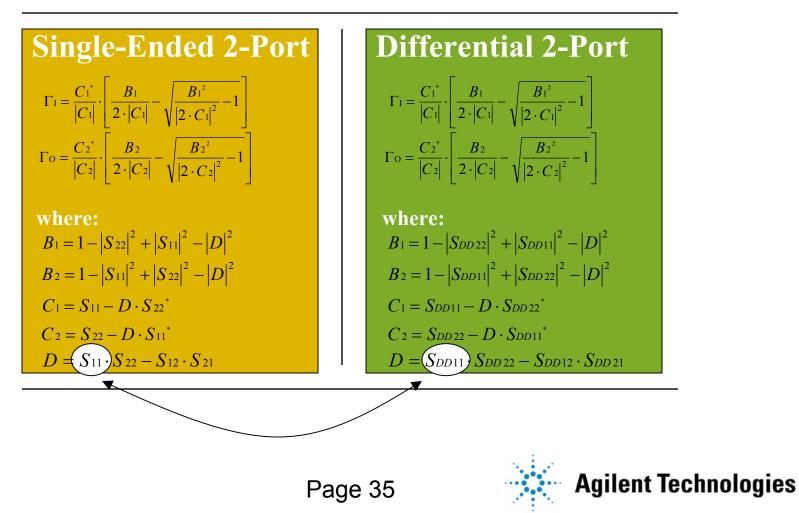
$$C_{1} = S_{DD11} - D \cdot S_{DD22}^{*}$$

$$C_{2} = S_{DD22} - D \cdot S_{DD11}^{*}$$

$$D = S_{DD11} \cdot S_{DD22} - S_{DD12} \cdot S_{DD21}$$
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Reduce performance of differential

Simultaneous Conjugate Match: Single-Ended vs. Differential



Balanced Device Design Methodology

- Matching example can be also be extended to other design considerations (K, MAG, VSWR, Z, etc.)
- Reason is parallel approach to parameter derivation
- For balanced device, use identical approach as singleended design
- Isolate balanced device to specific mode
 - Substitute parameters
 - Example: $(S_{nm} \rightarrow S_{DDnm})$

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Alternative Methods to Acquire Mixed-Mode S-Parameters



Standard 2-Port Network Analyzer

MultiPort Network Analyzer

Math (RF Circuit Simulation Software; e.g.ADS)

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MultiPort Network Analyzer with Balanced Measurement Capability

Mixed-Mode S-Parameters

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Agilent ENA Series Differential Measurement Network Analyzer



- Self-Contained Balanced Measurement Solution for R&D or Mfg
- 3 GHz and 8.5 GHz Versions; Up to 4 ports
- High Accuracy and Very High Speed

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4-Port Balanced Measurement System



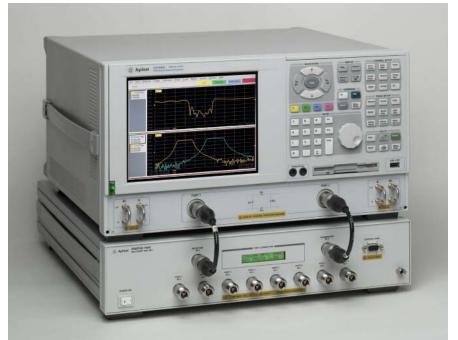


- Excellent R&D Solution
- Versions up to 20 GHz
- Time Domain option
- Agilent 8753, 8720, or PNA based solutions

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High Speed Multiport Device Test System





- Excellent for High Port Count (e.g. LTCC) Devices
- Semi-custom Agilent PNA based Solutions
- Meets Both R&D or High-Speed Manufacturing Needs

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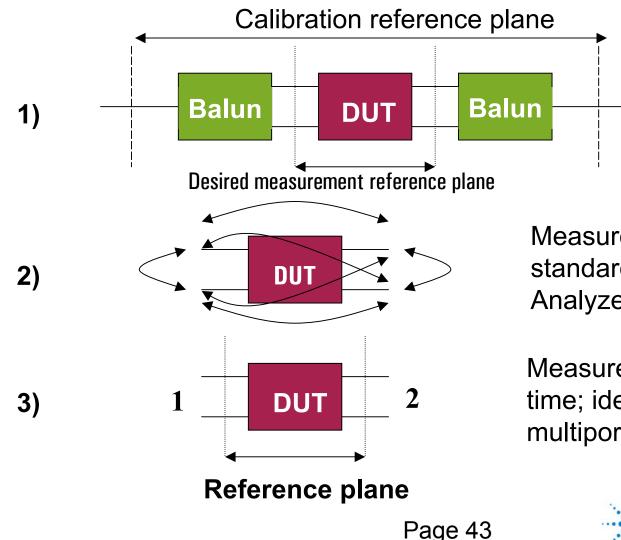


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Calibration For Each Measurement Alternative



Ideally calibrate after baluns but no balanced standards exist

Measure 2 ports at a time; use standard 2-port Network Analyzer Calibration process

Measure multiple ports at a time; ideally use sophisticated multiport Calibration process



RF Balanced Device Characterization Sophisticated Multiport Cal Eliminates

Redundant Connections of Cal Standards

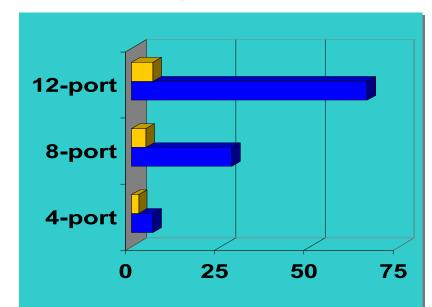
12-port 8-port 4-port 0 100 200 300 400

Reflection Connections

Sophisticated Multiport Cal

Traditional 2-port VNA Cal

Through Connections



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E-CAL (Electronic Calibration)

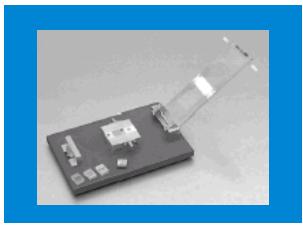
- Automated "system" for performing calibrations
- 2- and 4-port configurations
- Single connection
- Calibrates all paths/permutations
- Resulting calibration is traceable





Fixture Calibration

- Methods for removing fixture effects from measurement:
 - Port extensions
 - Delay
 - TRL calibration
 - In-fixture SOLT calibration
 - De-embedding
 - Soft fixturing





Agenda

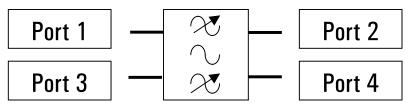
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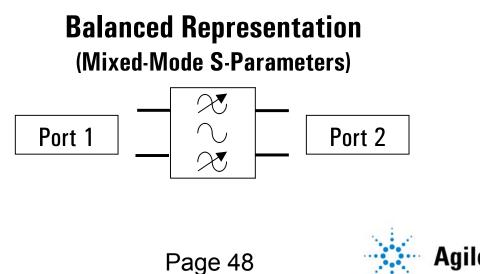


SAW Filter Measurement Example

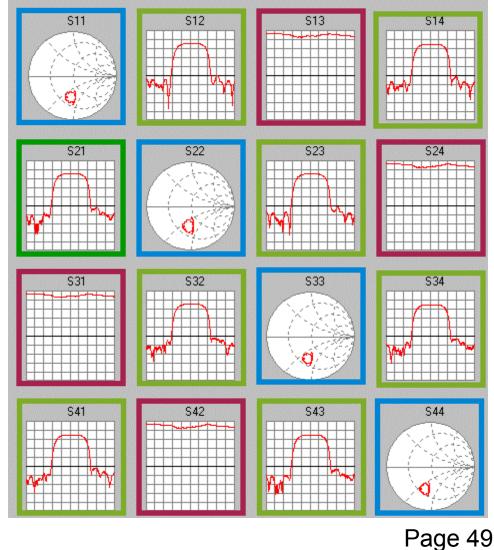
Single-Ended Representation

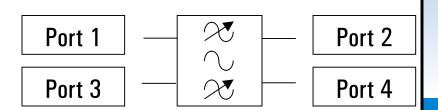
(Conventional S-Parameters)





Single-Ended SAW Filter Performance

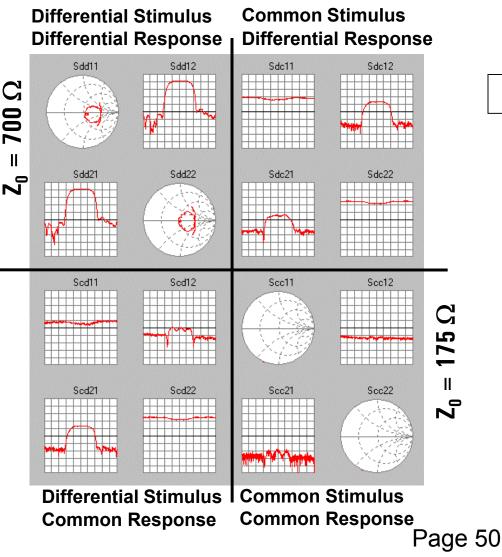


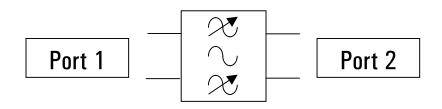


- Reference Z = 350Ω (all ports)
- Capacitive component to port matches
- Insertion loss (14.5dB)
- Input-input coupling
- Output-output coupling



Balanced SAW Filter Performance

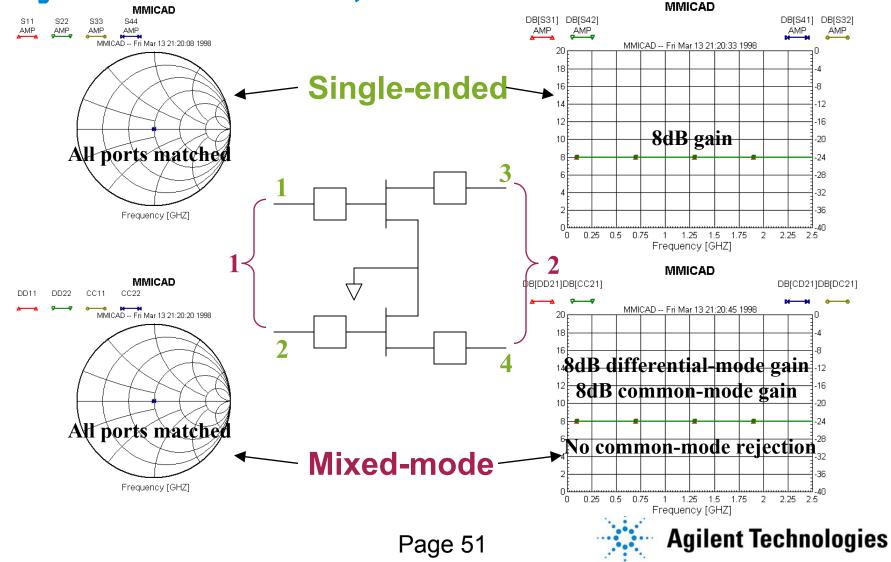




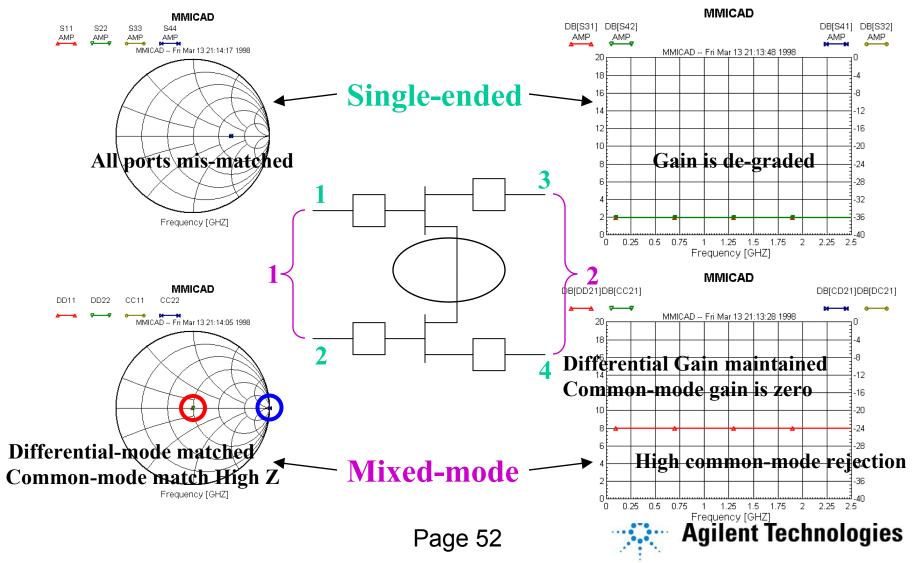
- Reference Z depends on mode
- Well-matched differentially
- Reflective in common mode
- Insertion loss (8.9dB)
- Mode conversion
- Common mode rejection (60dB)



Symmetric Circuit, Zero Ground Z



Symmetric Circuit, Infinite Ground Z



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Conclusion

- Balanced Devices present many new test challenges
- Mixed-mode S-parameters provide comprehensive characterization (D-D, C-C, D-C, C-D) of balanced ports/devices
- Traditional S-parameters may be misleading and lead to lower performance designs
- Dedicated balanced measurement solutions include sophisticated calculations and calibration



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